

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 30585/26	SERIAL NO. 09/666,110
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)			APPLICANT Korbin S. Van Dyke, et al.	
			FILING DATE September 20, 2000	GROUP ART UNIT 2183

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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RLE	4,514,803	4/30/1985	Agnew	364	200	Apr. 26 1982
RLE	4,575,797	03/11/1986	Gruner	364	200	May 22, 1981
RLE	5,307,504	04/26/1994	Robinson	395	800	Mar. 7, 1991
RLE	5,481,684	01/02/1996	Richter	395	375	Jul. 20 1994
RLE	5,481,693	01/02/1996	Blomgren	395	375	Jul. 20, 1994
RLE	5,542,059	07/30/1996	Blomgren	395	375	Dec. 21, 1994
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RLE	5,574,927	11/12/1996	Scantlin	395	800	Nov. 12, 1996
RLE	5,598,546	01/28/1997	Blomgren	395	385	Aug. 31, 1994
RLE	5,638,525	06/10/1997	Hammond	395	385	Feb. 10, 1995
RLE	5,652,869	07/29/1997	Herdeg	395	500	Dec. 28, 1994
RLE	5,685,009	11/04/1997	Blomgren	395	800	Nov. 29, 1995
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RLE	5,781,750	07/14/1998	Blomgren	395	500	Jan 11, 1994
RLE	5,842,017	11/24/1998	Hookway	395	707	Jan. 29, 1996
RLE	5,884,057	03/16/1999	Blomgren	395	375	May 8, 1995
RLE	5,903,760	05/11/1999	Farber	395	707	Jun. 27, 1996
RLE	5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996
RLE	6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996
RLE	6,031,992	02/29/2000	Cmelik	395	705	Jul. 5, 1996
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RLE	/ WO 96/24895 Hammond	08/15/1996	PCT/US WPO	606F	9/22		
RLE	WO 97/50031 Farber	12/31/1997	PCT/US WPO	606F	9/455		
RLE	/ WO 98/13740 Wing	04/02/1998	PCT/US WPO	606F	—		

EXAMINER Richard Ellis	DATE CONSIDERED 3/15/2004
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RCE	Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
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RCE	Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)
RCE	Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)
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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT Korbin S. Van Dyke, et al.			
				FILING DATE July 26, 2000		GROUP ART UNIT 2183	
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RUE	REJ	4,779,187	10/18/1988	Letwin	—	—	
RUE	REJ 20 2007	4,812,975	03/14/1989	Adachi et al.	—	—	
RUE	REJ 20 2007	5,115,500	05/19/1992	Larsen	—	—	
RUE	REJ 20 2007	5,335,331	08/02/1994	Murao et al.	—	—	
RUE	REJ 20 2007	5,568,646	10/22/1996	Jaggar	—	—	RECEIVED FEB 24 2003
RUE	REJ 20 2007	5,619,666	04/08/1997	Coon et al.	—	—	
RUE	REJ 20 2007	5,678,032	10/14/1997	Woods et al.	—	—	
RUE	REJ 20 2007	5,751,982	05/12/1998	Morley	—	—	Technology Center 2100
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RUE	REJ 20 2007	6,021,265	02/01/2000	Nevill	—	—	
RUE	REJ 20 2007	6,163,764	12/19/2000	Dulong et al.	—	—	
RUE	REJ 20 2007	6,219,774	04/17/2001	Hammond et al.	—	—	
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							NO
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EXAMINER		Richard Ellis		DATE CONSIDERED		3/19/2004	

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APPLICANT

Korbin S. Van Dyke, et al.

FILING DATE

August 20, 2000

GROUP ART UNIT

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↑	5,155,835	10/13/1992	Belsan	—	—	Nov. 19, 1990
	5,241,664	08/31/1993	Ohba	—	—	Jan. 7, 1991
	5,301,287	04/05/1994	Herrell	—	—	Feb. 16, 1993
	5,361,340	11/01/1994	Kelly	—	—	Mar. 9, 1993
	5,432,795	07/11/1995	Robinson	371	19	May 15, 1991
	5,454,117	09/26/1995	Puziol	395	800	Aug. 25, 1993
	5,515,518	06/07/1996	Stiles	395	375	Jul. 5, 1994
	5,608,886	03/04/1997	Blomgren	395	586	Aug. 31, 1994
	5,628,016	05/06/1997	Kukol	395	705	June 15, 1994
	5,636,366	06/03/1997	Robinson	395	490	Oct. 30, 1995
	5,659,679	08/19/1997	Alpert	395	183.1	May 30, 1995
	5,664,159	09/02/1997	Richter	395	500	May 8, 1995
	5,715,421	02/03/1998	Akiyama	—	—	Oct. 16, 1992
	5,721,927	02/24/1998	Baraz	395	705	Aug. 7, 1996
	5,754,878	05/19/1998	Asghar	395	800	Mar. 18, 1996
	5,774,287	06/30/1998	Leonhardt	—	—	Dec. 9, 1994
	5,781,792	07/14/1998	Asghar	395	800.35	Mar. 18, 1996
	5,784,640	07/21/1998	Asghar	395	800.35	Mar. 18, 1996
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	5,796,939	08/18/1998	Berc, et al.	395	184.01	Mar. 10, 1997
	5,802,272	09/01/1998	Sites, et al.	395	183.21	Dec. 19, 1994
	5,802,373	09/01/1998	Yates	395	705	Jan. 29, 1996
	5,822,578	10/13/1998	Frank	395	591	June 5, 1995
	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996
	5,838,962	11/17/1998	Larson	395	586	Apr. 9, 1997
	5,838,978	11/17/1998	Buzbee	395	705	Oct. 9, 1996
	5,847,955	12/08/1998	Mitchell	—	—	Jun. 23, 1997
	5,901,308	05/04/1999	Cohn	—	—	Mar. 18, 1996
↓	5,905,855	05/18/1999	Klaiber	395	183.07	Feb. 28, 1997
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↑	5,949,415	09/07/1999	Lin	345	335	Jun. 16, 1997
	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997
	5,958,061	09/28/1999	Kelly	714	1	Jul. 24, 1996
	5,999,736	12/07/1999	Gupta	—	—	May 9, 1997
	6,003,038	12/14/1999	Chen	707	103	March 31, 1997
	6,005,551	12/21/1999	Osborne	—	—	Apr. 25, 1997
	6,011,908	01/04/2000	Wing	395	182-17	Dec. 23, 1996
	6,014,735	01/11/2000	Chennupaty	712	210	Mar. 31, 1998
	6,021,489	02/01/2000	Poplingher	712	239	Jun. 30, 1997
	6,026,478	02/15/2000	Dowling	—	—	Dec. 23, 1997
	6,029,004	02/22/2000	Bortnikov	—	—	Mar. 17, 1997
	6,032,247	02/29/2000	Asghar	712	35	Nov. 14, 1997
	6,035,393	03/07/2000	Glew	712	237	May 7, 1998
	6,044,221	03/28/2000	Gupta	—	—	May 9, 1997
	6,070,009	05/30/2000	Dean	—	—	Nov. 26, 1997
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	Re. 36,766	07/04/2000	Krauskopf	711	203	Aug. 21, 1992
	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996
	6,154,857	11/28/2000	Mann	—	—	Dec. 17, 1997
	6,157,970	12/05/2000	Gafken	—	—	Sep. 24, 1997
	6,175,957	01/16/2001	Ju	—	—	Dec. 9, 1997
	6,226,789	05/01/2001	Tye	—	—	Jan. 29, 1996
	6,237,073	05/22/2001	Dean	—	—	Nov. 26, 1997
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	6,253,215	06/26/2001	Agesen	—	—	Aug. 17, 1998
	6,351,784	02/26/2002	Neal	—	—	Dec. 28, 1998
	6,374,369	04/16/2002	O'Donnell	—	—	May 21, 1999
	6,397,379	05/28/2002	Yates	—	—	Oct. 28, 1999
	6,405,327	06/11/2002	Sipple	—	—	Aug. 19, 1998
↓	6,463,582	10/08/2002	Lethin	—	—	Oct. 21, 1998
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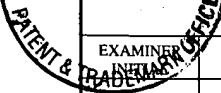
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RLE	6,496,922	12/17/2002	Borrill	—	—	Oct. 31, 1994

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						YES	NO
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↑	WO 96/24895-Hammond	08/15/1996	PCT/US W/PO	G06F	—	9/22	
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	WO 98/38575-Klaiber	09/03/1998	PCT/US W/PO	G06F	—	11/00	
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	WO 99/03037-Kelly	01/21/1999	PCT/US W/PO	G06F	—	11/00	
↓	WO 99/08188-Kelly	02/18/1999	PCT/US W/PO	G06F	—	11/00	
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<p style="text-align: center;">JUN 19 2003 PATENT & TRADEMARK OFFICE RCE</p> <table border="1"> <tr><td>J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)</td></tr> <tr><td>Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm</td></tr> <tr><td>Ebciooglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)</td></tr> <tr><td>Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)</td></tr> <tr><td>Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)</td></tr> <tr><td>Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)</td></tr> <tr><td>Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)</td></tr> <tr><td>R.E. Hank, et al., Proceeding of the 28th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)</td></tr> <tr><td>Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM</td></tr> <tr><td>Intel Corporation, Intel Architecture Software Developer's Manual, <i>passim</i> (1997)</td></tr> <tr><td>Kavi et al., A Performability Model for Soft Real-Time Systems, IEEE Jan 1994</td></tr> <tr><td>Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)</td></tr> <tr><td>K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)</td></tr> <tr><td>Nanodata Corporation, QM-1 Hardware level User's Manual (1975)</td></tr> <tr><td>Nanodata Corporation, QM-2 Processor (1976)</td></tr> <tr> <td><i>RCE</i></td> <td colspan="5">Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)</td> </tr> <tr> <td>EXAMINER <i>Richard Ellis</i></td> <td colspan="5">DATE CONSIDERED <i>3/15/2004</i></td> </tr> </table>						J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28 th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)	Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm	Ebciooglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)	Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)	Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)	Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)	Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)	R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)	Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM	Intel Corporation, Intel Architecture Software Developer's Manual, <i>passim</i> (1997)	Kavi et al., A Performability Model for Soft Real-Time Systems, IEEE Jan 1994	Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)	K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)	Nanodata Corporation, QM-1 Hardware level User's Manual (1975)	Nanodata Corporation, QM-2 Processor (1976)	<i>RCE</i>	Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)					EXAMINER <i>Richard Ellis</i>	DATE CONSIDERED <i>3/15/2004</i>				
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FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 005231/26-0051BS		SERIAL NO. 09/666,110	
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				APPLICANT Korbin S. Van Dyke, et al.			
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U.S. PATENT DOCUMENTS							
EXAMINER <i>TRADEMAKERS</i> <i>19 2003</i> <i>USCA 2</i>	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)							
RUE	Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)						
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EXAMINER <i>Richard Ellis</i>				DATE CONSIDERED <i>3/15/2004</i>			